



Comparison between software and hardware control in PSM mode of a Buck converter

Abdellah El jounaidi, Abderraouf Aboudou, Abderrafia Sabir

(Department Génie Electrique, ESTC/ Hassan II University, Casablanca, Morocco)

Abstract: The control of a DC-DC converter is generally done through a circuit comprising logic and analog components. In the presented work, we have ordered a Buck converter in two different ways. The first method consists in using a control in the form of an electrical circuit. The second method is based on the control of the converter through a programmable circuit. The program introduced in the form of a MATLAB embedded function and simulated under SIMULINK / MATLAB gave results similar to those obtained using the control made with a conventional electrical circuit. For the control, we chose the PSM mode (Pulse Skipping Modulation). It is an efficient and simple control to realize both in the form of an electrical circuit and under programming. The comparison of the results obtained under SIMULINK / MATLAB enabled us to highlight the advantages and disadvantages of each method.

Keywords: Buck Converter, PSM Mode, Programmable control, MATLAB embedded function.

I. INTRODUCTION

Static converters are generally based on power electronics components used as switches. They have a control circuit which must act on the opening or closing of the switch according to a well-defined protocol and frequency. These control devices are implemented in the form of analogue or digital circuits. At the present time, we are moving towards a programmed control which is managed by a programmable logic circuit. The advantage of this method is its simplicity and versatility compared with a control implemented in the form of an electrical circuit.

The work presented aims to make a comparison between the control of a Buck converter, realized in the form of an electrical circuit and the programmed control managed by a programmable circuit. The control mode chosen is the so-called Pulse Skipping Modulation (PSM) mode. This command is often carried out in the form of a logic circuit controlled by a clock which will generate the pulses supplied to the converter switch. In the case of programmed control, the electrical circuit will be replaced by a program which operates on the same principle and must achieve the same result once it has been installed on a programmable circuit as a microcontroller type.

We will simulate the response of the converter submitted to the two types of control using SIMULINK / MATLAB, the objective being to make a comparison of the results obtained to highlight the advantages and disadvantages of each method.

II. BUCK CONVERTER AND PSM CONTROL

2.1 Buck Converter

The electrical scheme of the DC-DC converter used is shown in figure1:

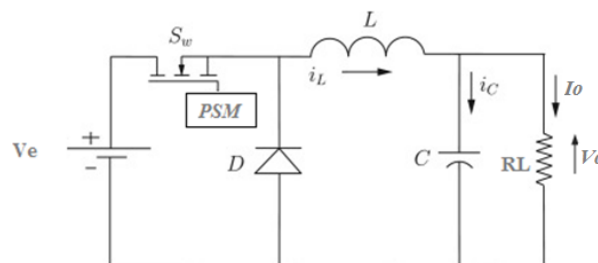


Fig. 1 Buck Converter

It is a Buck converter controlled by a PSM type impulse control device [1] [2]. The mode of operation adopted is that of Continuous Conduction Mode (CCM) [3], [4], [5].

The PSM regulator which will supply the pulses to the switch will act on the converter according to the result of comparison of the reference voltage (V_{ref}) and the output voltage (V_o) taken at the load R_L . It is introduced in the form of a MATLAB function which will apply to the switch (MOSFET) a clock signal H. The



clock frequency f_h and the duty cycle α remain fixed. When the load R_L output voltage (V_o) is lower than the reference voltage (V_{ref}), the pulse train is sent to the switch, which will result in an increase in voltage across the load, that is the charging period of the capacitor. When V_o exceeds V_{ref} the pulses are blocked and the opening of the switch causes discharge of the capacitor and consequently the decrease of the voltage across the load. When the output voltage (V_o) becomes lower than the reference voltage (V_{ref}), the pulse train is applied again to the switch and the voltage V_o increases. Thus, when the steady state is established, the output voltage (V_o) will oscillate periodically around the reference voltage (V_{ref}) with a ripple whose amplitude will be greater or less according to the value of the load resistor R_L .

2.2 Modelisation of PSM function

The operating principle of the PSM control is summarized in the flow chart of figure 2. V_{ref} , V_o and H represent the input variables of the function and D the output variable [6].

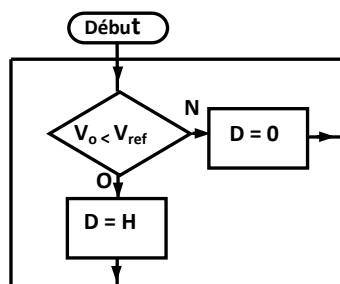


Fig. 2 PSM function flow chart

When the steady state is established after a transient phase, the clock signal with period T_h and duty cycle α will be applied to the MOSFET when $V_o < V_{ref}$ (charge period of the capacitor) and blocked in the opposite case (period of discharge). During the charging period, the MOSFET transistor will be on during the time αT_h and blocked during the time $(1-\alpha) T_h$. If p is the number of cycles during which the clock signal is applied to the transistor and q the number of cycles during which the clock signal is absent as shown in figure 3, the effective switching frequency f_c of the MOSFET can be expressed by the relation [7], [8]:

$$f_c = \frac{p}{p+q} f_h \quad (1) \quad \left(f_h = \frac{1}{T_h} \right)$$

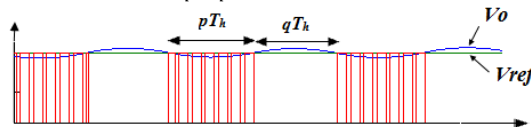


Fig. 3 Evolution of V_o and the PSM control signal in steady state

The modulation factor M is defined by the relation:

$$M = \frac{q}{p+q} = 1 - \frac{f_c}{f_h} \quad (2) \quad (0 \leq M \leq 1)$$

It is shown that in the case of the continuous conduction mode (CCM), the mean value of the voltage V_o applied to the resistor R_L is given by:

$$V_o = (1 - M)\alpha V_e \quad (3)$$

α is the duty cycle of the clock signal.

From the relation (6), the average current I_o circulating through R_L is deduced:

$$I_o = \frac{V_s}{R_L} = (1 - M)\alpha V_e / R_L \quad (4)$$

It can be seen that only the modulation factor M can vary since the other parameters are constant. It is this variation which makes it possible to adjust the output voltage V_o to the reference voltage V_{ref} .

It is also shown that to remain in the continuous conduction regime, the load resistor R_L must satisfy the condition:

$$R_L \leq \frac{2Lf_h}{1-\alpha} \quad (5)$$

For a given R_L load, the inductance L must satisfy the condition:

$$L \geq \frac{R_L}{2f_h} (1 - \alpha) \quad (6)$$

In our simulation, we considered the charge R_L between 1Ω and 50Ω , $\alpha = 0.9$ and $f_h = 100\text{kHz}$, $V_e = 12\text{V}$ and

$C = 100\mu\text{F}$.

From the relation (9), the minimum value of L is deduced:

$$L_{min} = 25\mu\text{H}$$

In the simulation we took $L = 200\mu\text{H}$.

III. MATLAB SIMULATION OF PSM-CONTROLLED CONVERTER RESPONSE

3.1 Buck Converter under SIMSCAPE

The electrical circuit of the converter and the PSM control function are realized under MATLAB / SIMULINK in the form of two separate blocks, as shown in the figure 4:

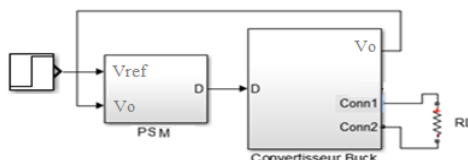


Fig. 4 Buck Converter and PSM control

The converter is realized under SIMSCAPE as shown in figure 5.

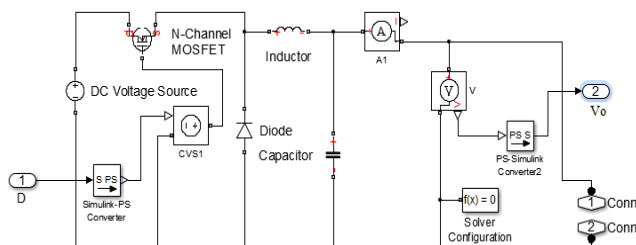


Fig.5 Buck Converter under SIMSCAPE

The PSM function which translates the flowchart of figure 2 is introduced in the form of a MATLAB embedded function $D = f(V_{ref}, V_o, H)$ whose inputs are the reference voltage (V_{ref}), the output voltage (V_o) The clock signal (H) [6]. Output D will send the clock signal H to the converter as long as $V_o < V_{ref}$ and block it when $V_o > V_{ref}$.

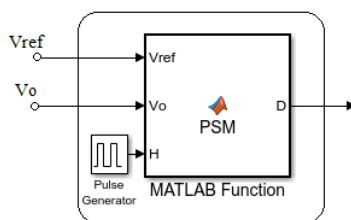


Fig. 6 PSM Function

The advantage of this software solution with respect to a hardware implantation is the simplification of the control circuit. A single microcontroller, containing the control program, is sufficient instead of a circuit comprising several logic gates and a clock circuit.

3.2 The hardware PSM control

The PSM function can also be performed using a logic circuit[9] as shown in the figure 7. When $V_{ref} - V_o > 0$, the output of the comparator (Compare to Zero) passes to 1 ($D = 1$, D input of the D Latch), the output Q of the D Latch passes to 1 and the output of the AND gate will reproduce the clock signal. When $V_{ref} - V_o < 0$, the input D of the D Latch passes to zero and subsequently the output Q , which will result in the blocking of the clock signal (output of the AND gate goes to zero).

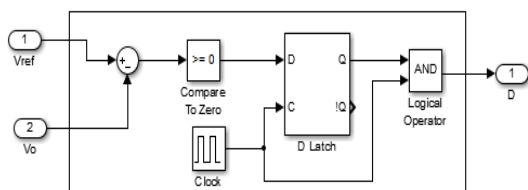


Fig.7 Control circuit

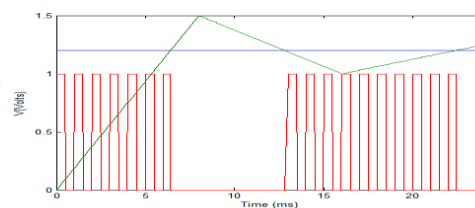


Fig.8 Evolution of PSM signal

The figure 8 Shows the evolution of the PSM signal. When $V_o < V_{ref}$ the clock signal is applied to the output of the circuit and when $V_o > V_{ref}$ the clock signal is blocked.

IV.SIMULATION OF THE CONVERTER RESPONSE

4.1 Form of the converter response to a constant V_{ref} voltage

Figure 9 shows the evolution of the output voltage (V_o) and the control signal PSM. It should be noted that after a transient phase, the output voltage will tend towards the reference voltage with oscillations that are greater or less depending on the value of the load, this is the steady state.

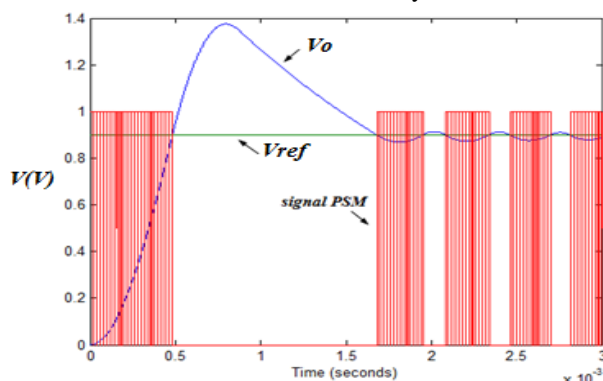


Fig. 9 Evolution of V_o and the PSM signal versus time

4.2 Comparison of the converter responses for the two types of control

Figure10 and figure11 Show the evolution of the output voltage V_o at the load R_L in response to a voltage step of amplitude 2V and for two different values of R_L .

The voltage step (V_{ref}) appears after a delay of one millisecond. We have plotted on the same figure the shape of the output voltage V_o at the output for the two types of control.

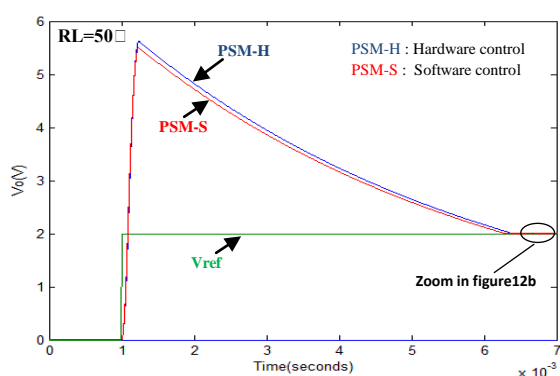


Fig.10 Evolution of the output voltage V_o with $R_L = 50\Omega$ and $V_{ref} = 2V$

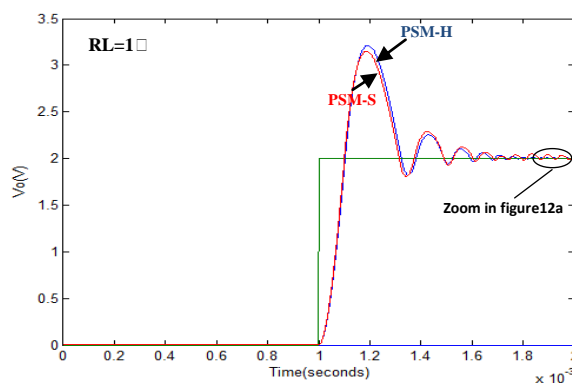


Fig.11 Evolution of the output voltage V_o with $R_L = 1\Omega$ and $V_{ref} = 2V$

We notice that the curves are almost confounded and converge towards the reference voltage (V_{ref}) more rapidly in the case where $R_L = 1\Omega$. We note also the occurrence of oscillations for low R_L . This is explained by the decrease in the damping coefficient of the oscillating circuit constituting the converter when the value of the load resistance R_L decreases.



On the other hand, if we take a closer look at the steady state, we notice that the ripples are larger in the case of the programmed PSM control ($\Delta V_o = 62\text{mV}$) than in the other type of control ($\Delta V_o = 7\text{mV}$) as shown in figure 12 (a).

The difference between the ripples from the two types of control decreases considerably for $R_L = 50\Omega$ as shown in (Figure12(b)). We observe $\Delta V_o = 2\text{mV}$ in the case of programmed control against $0,8\text{mV}$ for the hardware type of control.



Fig.12 Oscillations of output voltage V_o in steady state at $V_{ref} = 2\text{V}$

For a larger amplitude step (8V), it is also observed that the curves are almost coincident. We also note a lower oscillations for $R_L = 1\Omega$ (figure 14) compared to those of figure 11.

It should also be noted that the response time decreases considerably when the amplitude of the step increases, mostly for $R_L = 50\Omega$ as shown in figure 10 and figure 13.

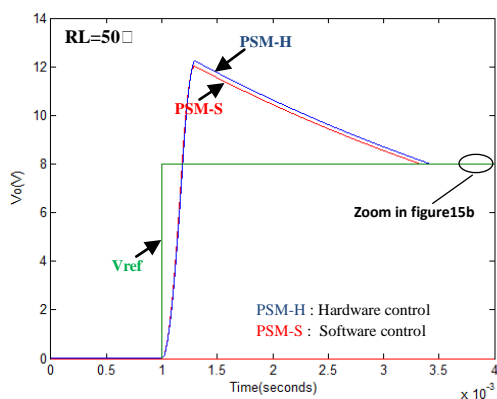


Fig.13 Evolution of the output voltage V_o with $R_L = 50\Omega$ and $V_{ref} = 8\text{V}$

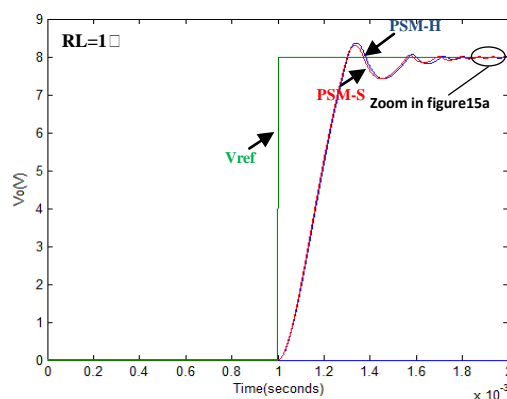


Fig.14 Evolution of the output voltage V_o with $R_L = 1\Omega$ and $V_{ref} = 8\text{V}$

In the steady state, we note that as in the previous case, for $R_L=1\Omega$, the ripples are larger in the case of the programmed PSM control ($\Delta V_o = 38\text{mV}$) than in the other type of control ($\Delta V_o = 10\text{mV}$) as shown in figure 15 (a). The difference between the ripples from the two types of control decreases considerably for $R_L = 50\Omega$. (Figure15(b)).



Fig.15 Oscillations of output voltage V_o in steady state at $V_{ref} = 8\text{V}$

The ripple dimming for large R_L values is a characteristic of PSM mode control [9].



V. CONCLUSION

In this work, we studied and compared the behavior of a Buck converter controlled with Pulse Skipping Modulation mode (PSM). The control was carried out according to two different approaches. The first type of PSM control has been introduced in the form of a MATLAB embedded function whose output will provide a control signal which will be applied to the converter. The second type of control consists of a conventional electrical circuit made with logic gates and a clock which will supply the control signal to the converter. We compared the responses of the converter with the two types of control and found a strong similarity in both cases. The difference lies in the amplitude of the ripples of output voltage at steady state, especially for the lower values of the load resistance R_L . Ripples at steady state are more important under programmed control, this is the main defect of this type of control. The advantage of the programmed control is also its simplicity and flexibility, contrary to a conventional control. Thus, for example, the clock frequency, can be modified by programming without changing any components.

The other advantage is the economics of the components, so a simple microcontroller can replace all the electrical circuit of the PSM control realized with a clock circuit and several logic gates.

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